WHAT IS CLAIMED IS:

1. An image processing apparatus comprising:

a sensor including a plurality of pixels each including a light receiving element, and a scanning circuit for reading out signals in time sequence from the plurality of pixels; and

a drive circuit which supplies pulses for driving said scanning circuit,

wherein said drive circuit is so arranged to output at least a first pulse and a second pulse smaller than the first pulse, and said drive circuit supplies the first pulse to said scanning circuit when a first resolution is selected, and supplies the first pulse and the second pulse to said scanning circuit when a second resolution lower than the first resolution is selected.

2. An apparatus according to claim 1, wherein when the second resolution is selected, said drive circuit supplies the first pulse in every other pulse or in every plurality of pulses.

3. An apparatus according to claim 2, further comprising a signal processing circuit which performs image processing on the basis of signals which are read out by supplying the first pulse to said scanning circuit.

4. An apparatus according to claim 2, wherein said sensor is formed on the same semiconductor chip, and a plurality of said sensors are mounted on a mount board.

5. An apparatus according to claim 1, wherein each of said pixels has an amplifying device which amplifies a signal from the light receiving element, and which outputs the amplified signal, a reset switch for resetting an input portion of said amplifying device, and a selecting switch for selectively reading the signal from said amplifying device, said selecting switch being supplied with a pulse from said scanning circuit.

6. An apparatus according to claim 1, further comprising a control circuit for switching between the first resolution and the second resolution.

7. An apparatus according to claim 1, further comprising a light source for irradiating light on said sensor, and a transport member for moving an original and said sensor relative to each other.